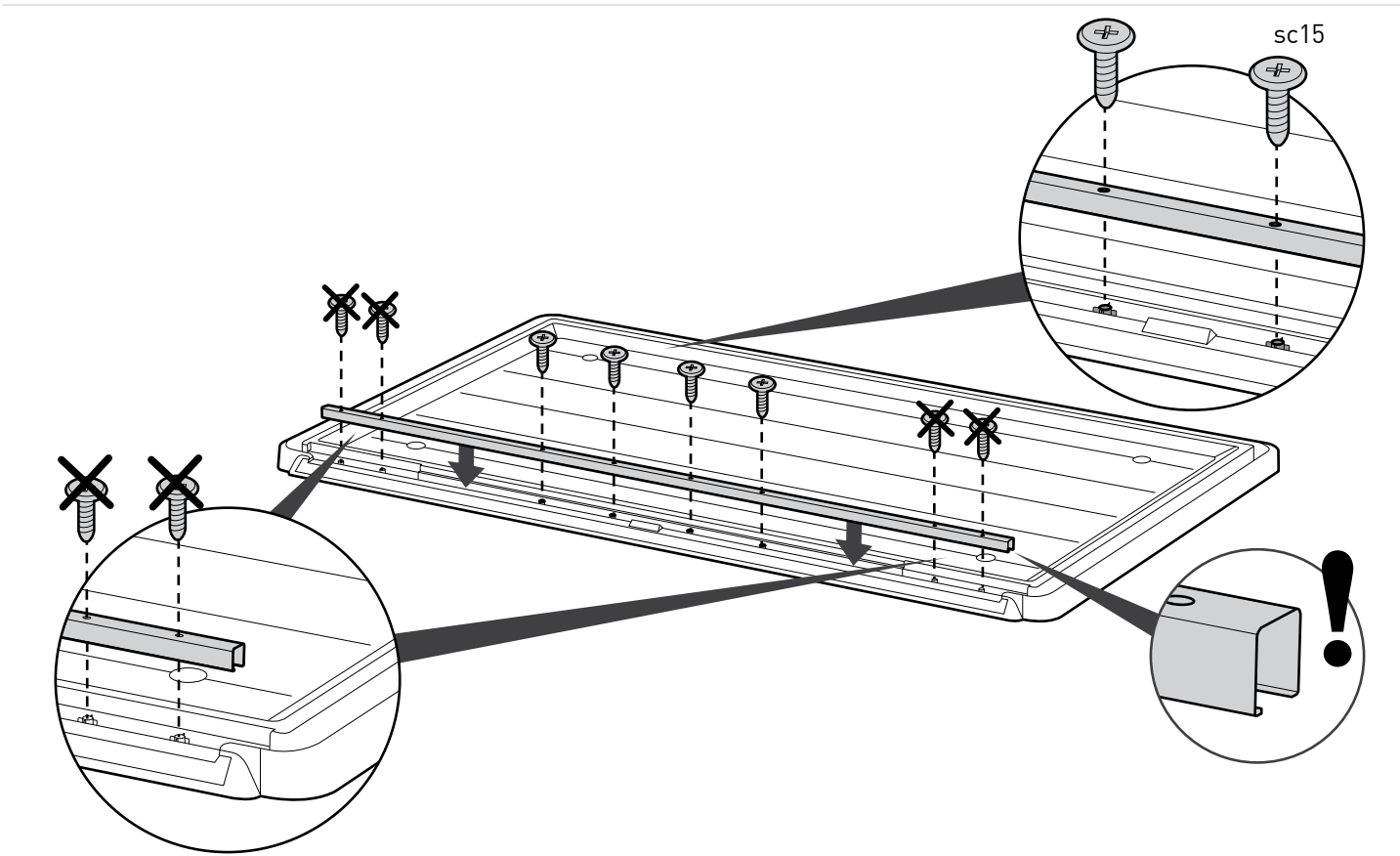


1

 ESF (x1)

 ESMF (x1)

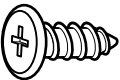
 sc15 (x4)

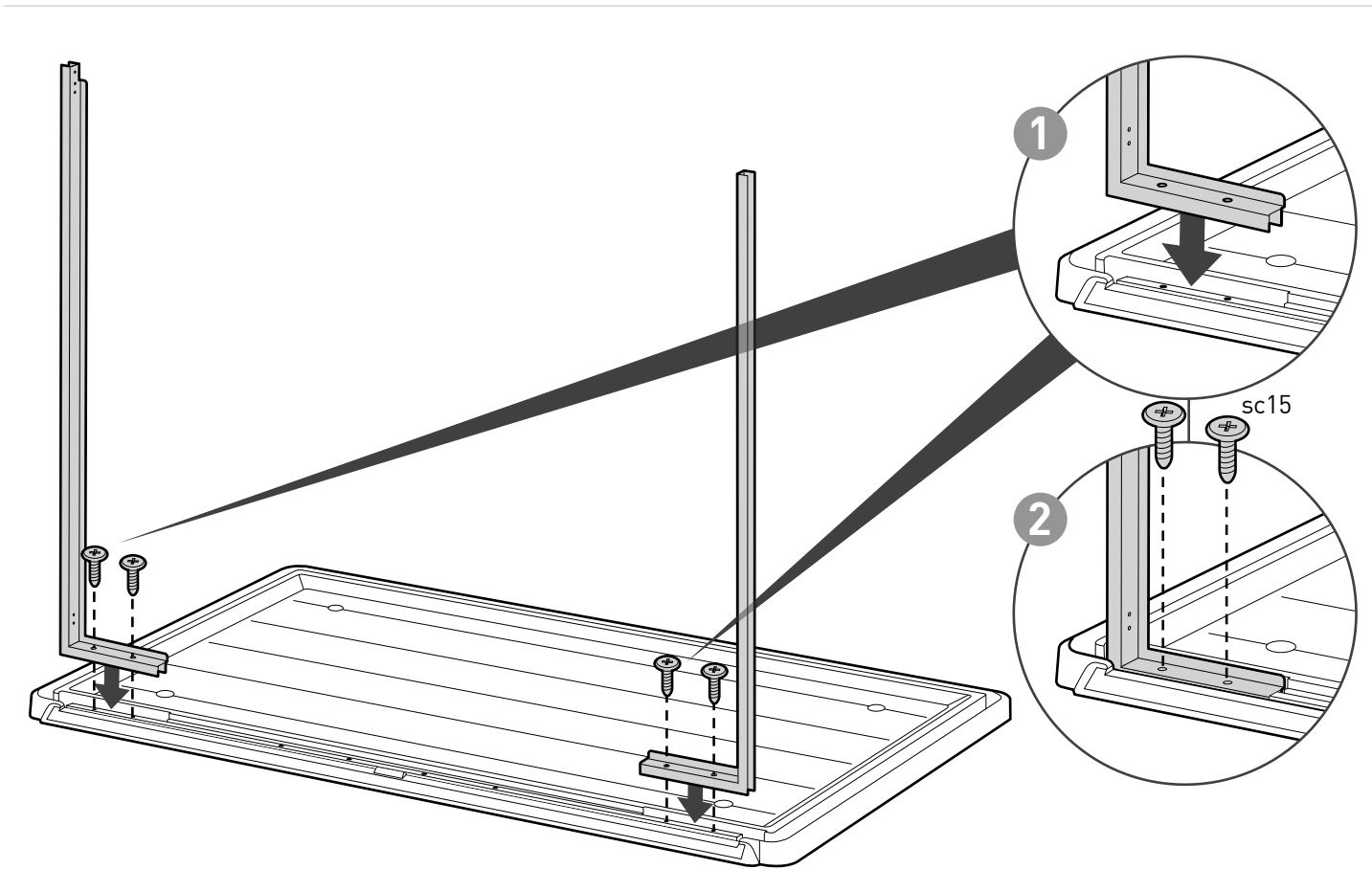


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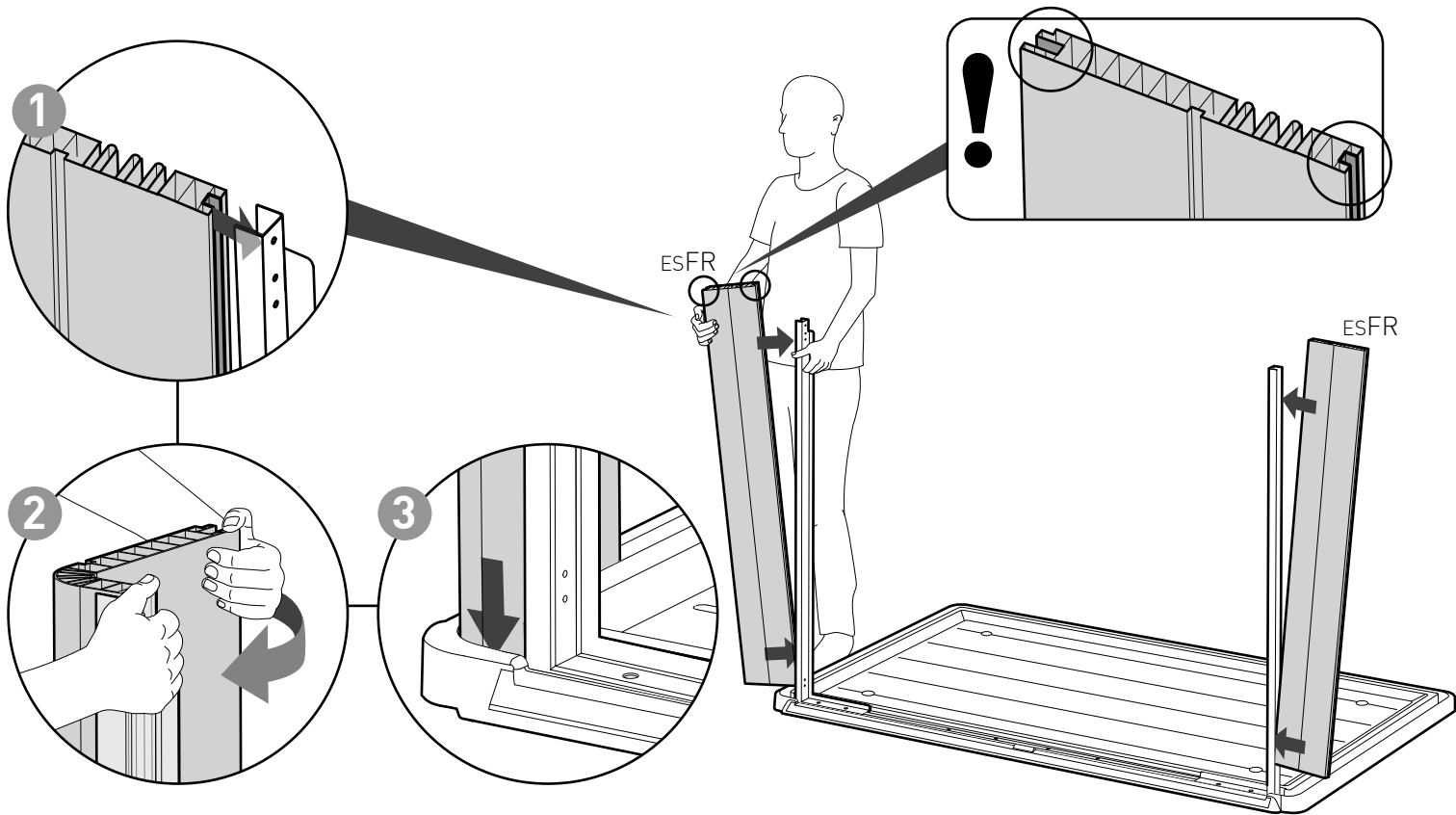
 ESLM (x1)

 ESRM (x1)

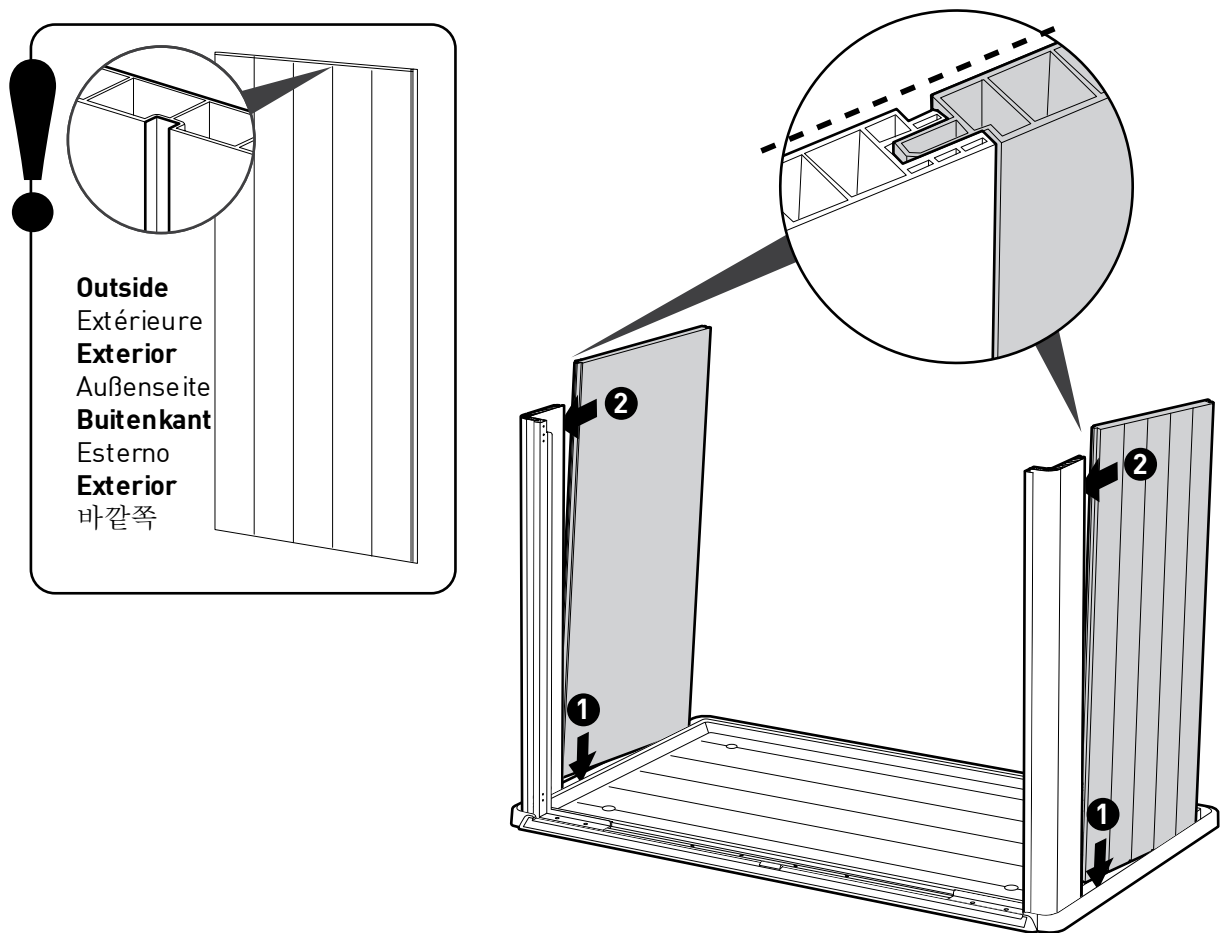
 sc15 (x4)



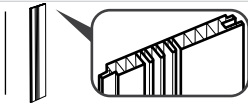
3 | esFR (x2)



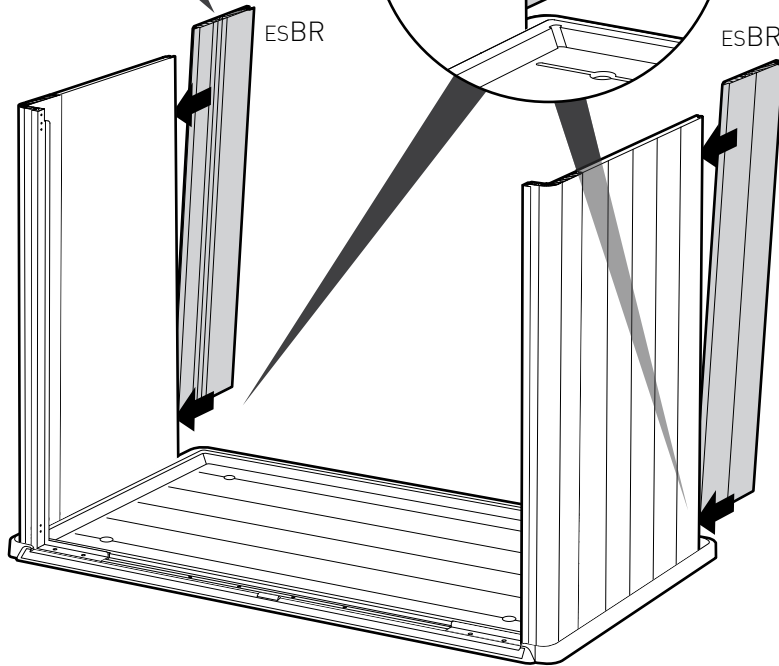
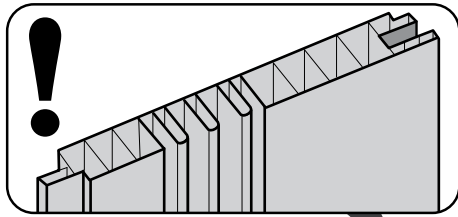
4 | ESP (x2)



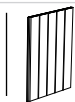
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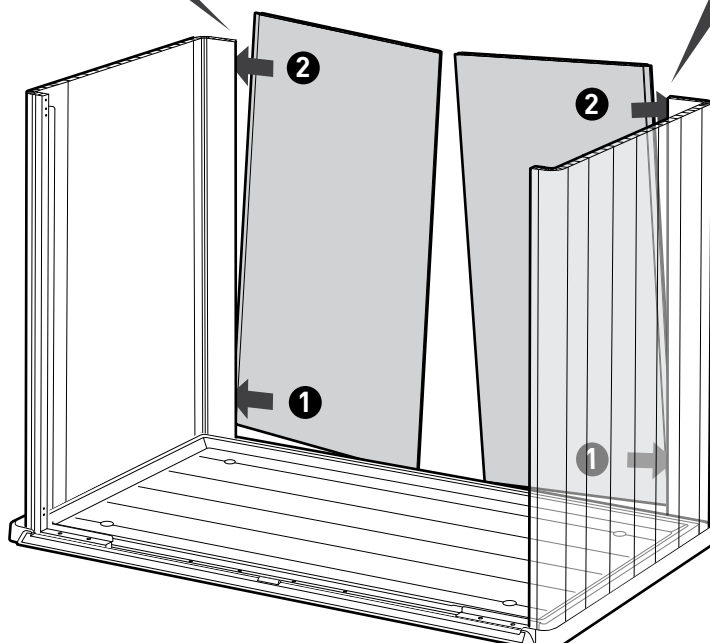
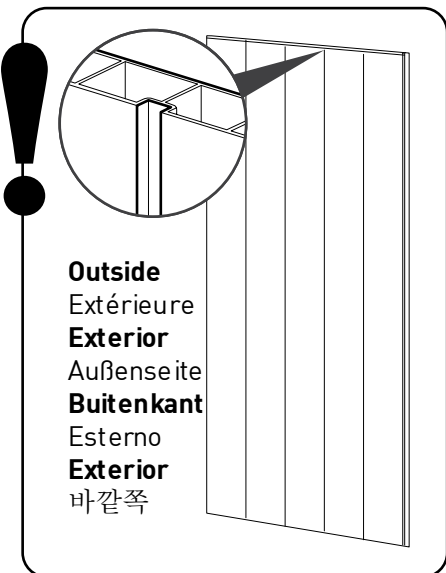
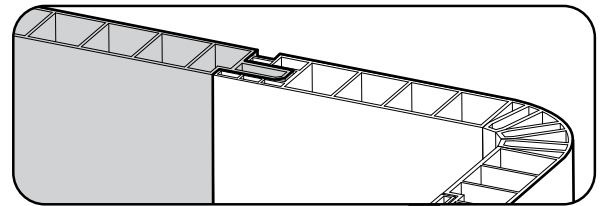
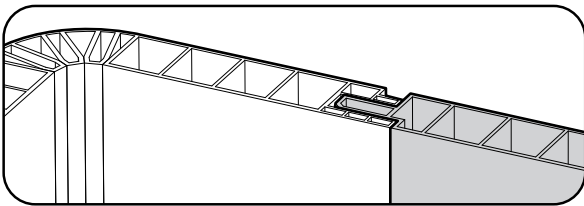
ESBR (x2)



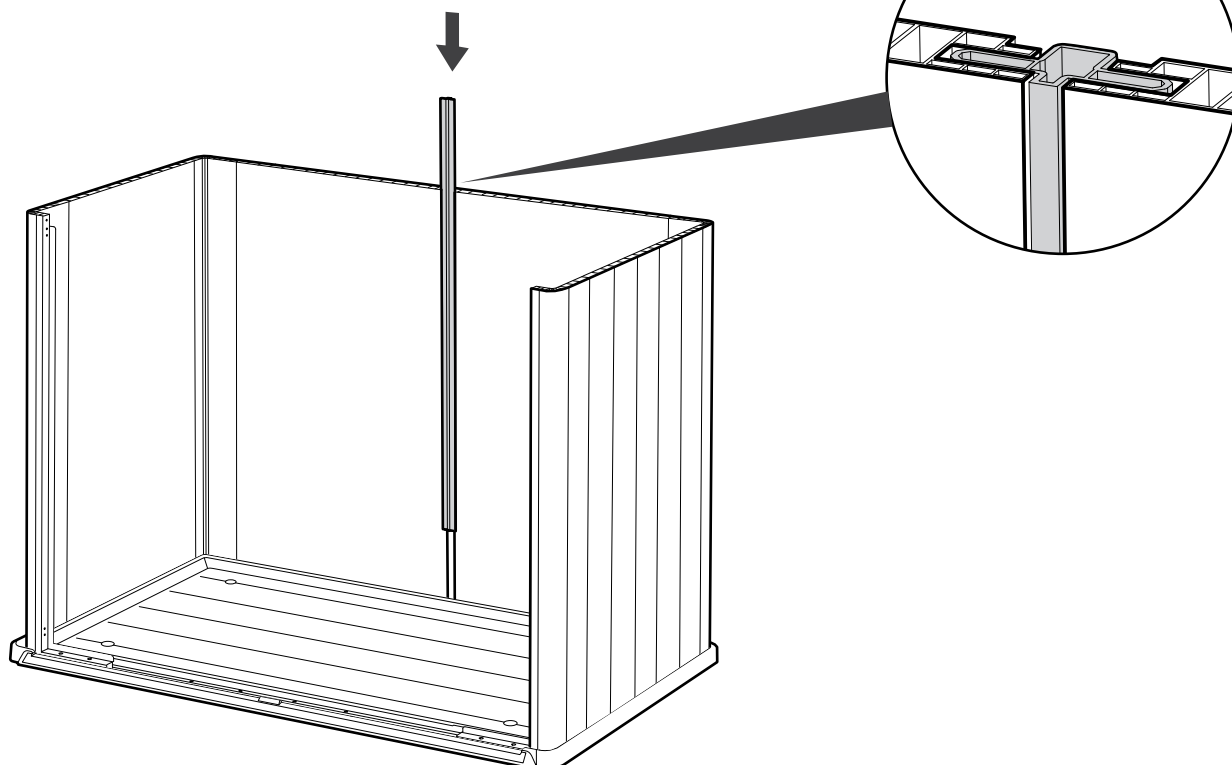
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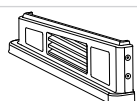
ESP (x2)



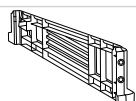
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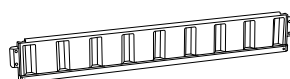
8



ESRT (x1)



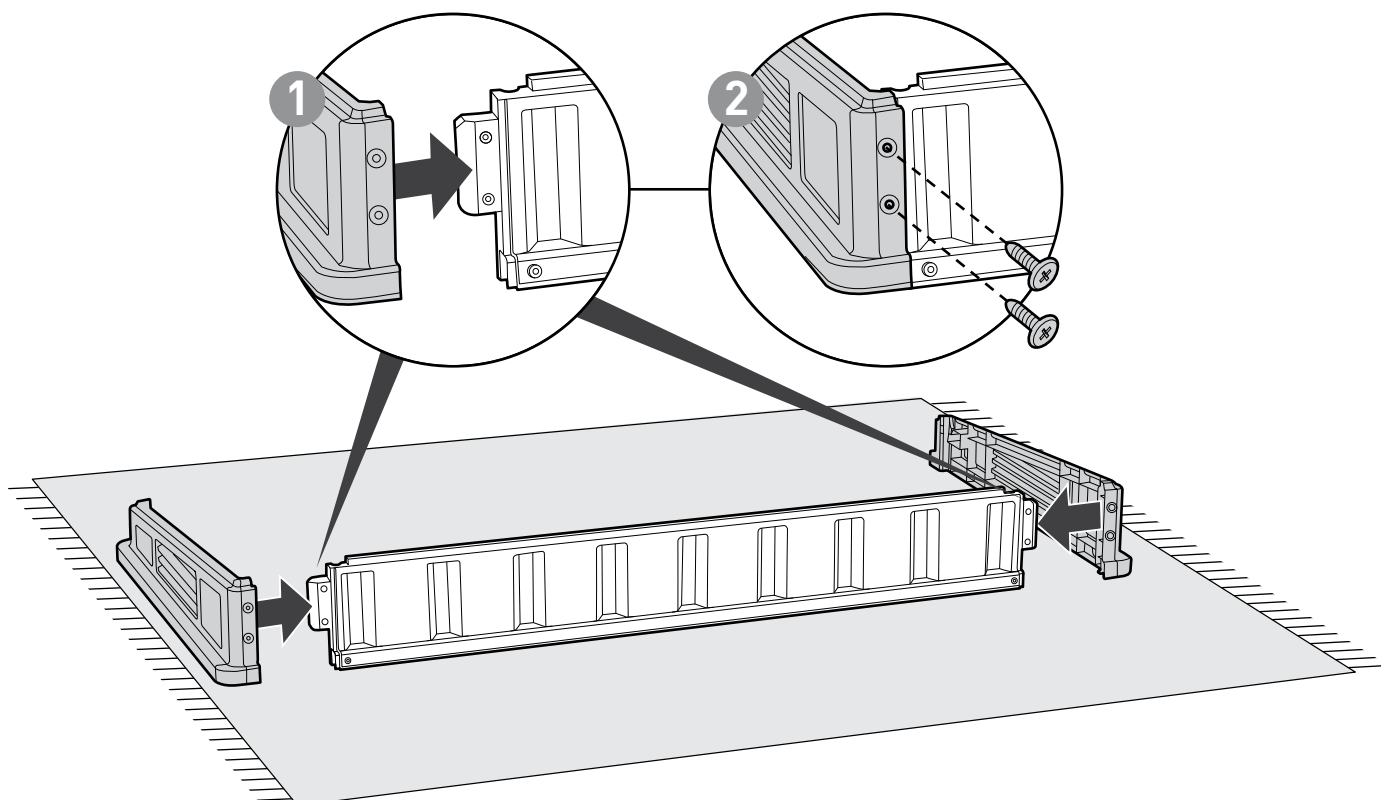
ESLT (x1)



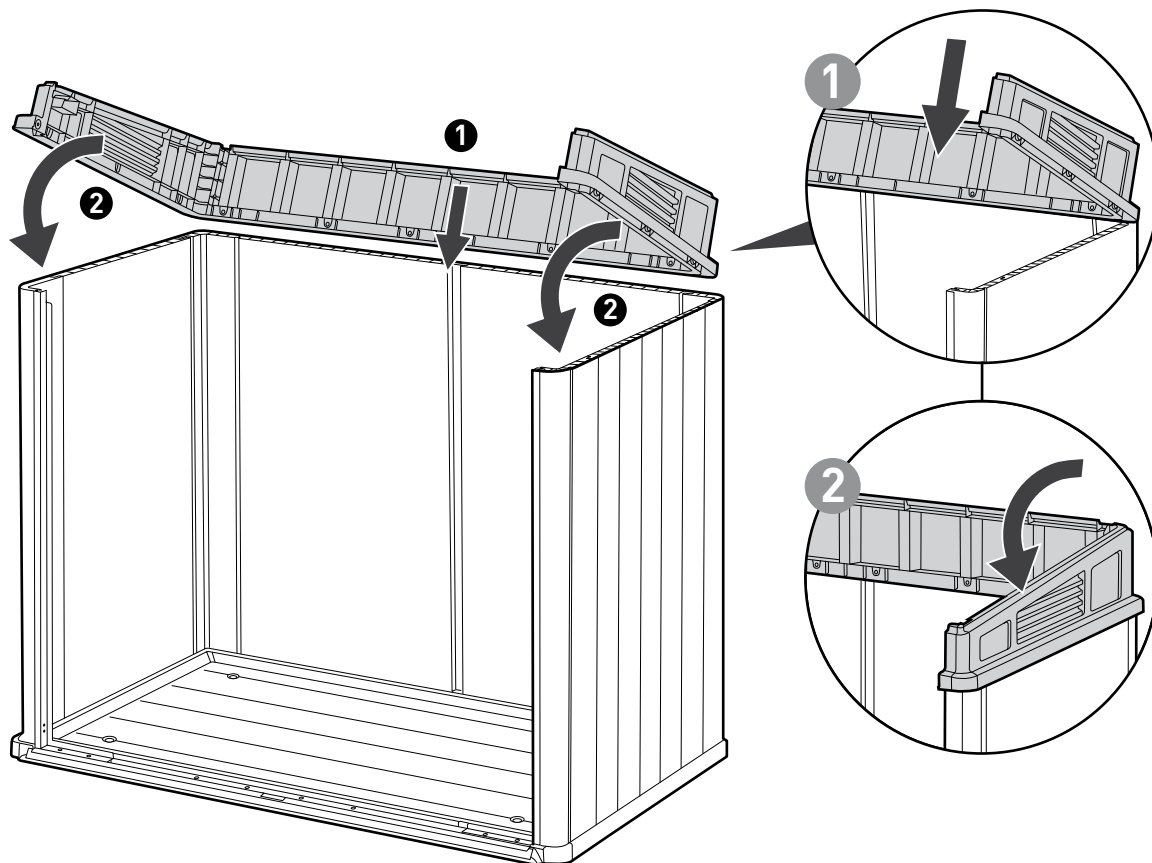
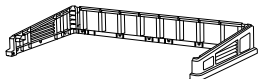
ESCB (x1)



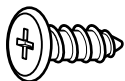
sc15 (x4)



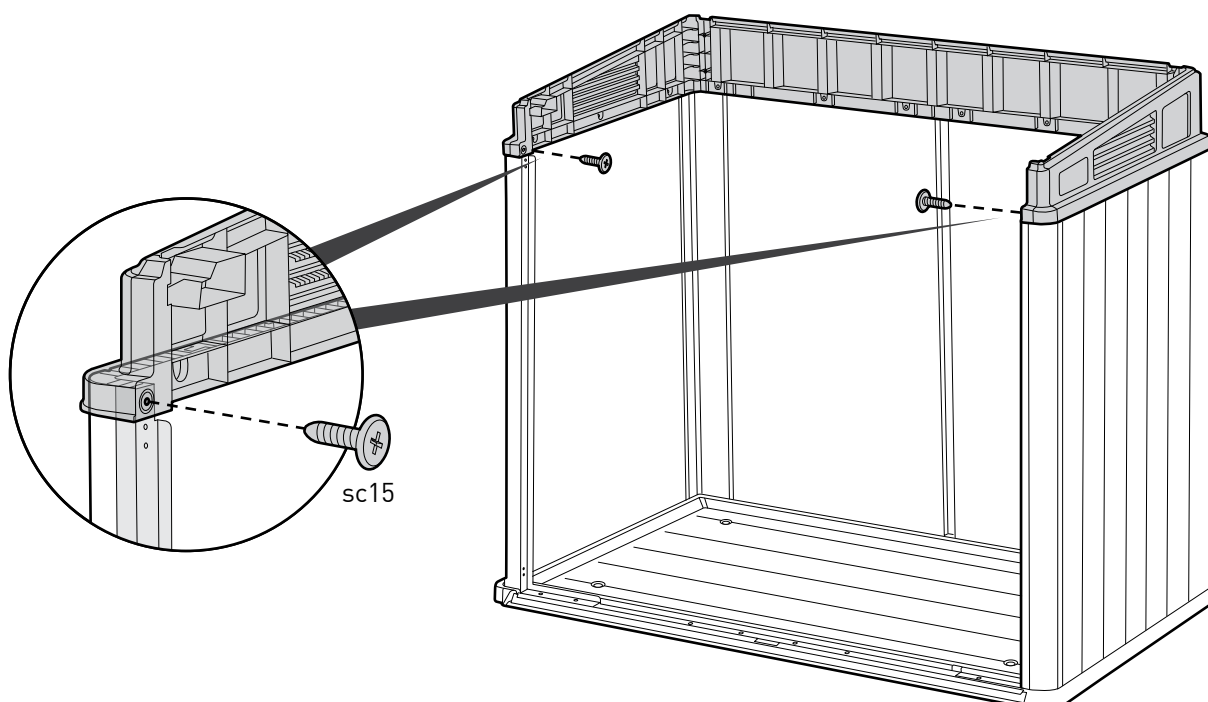
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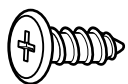
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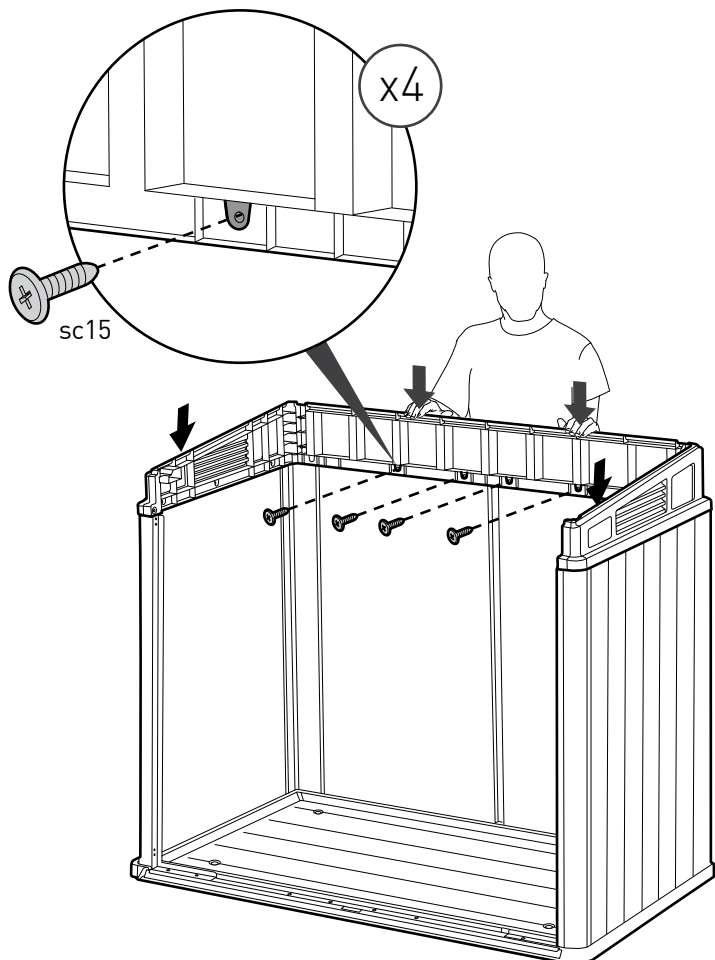
sc15 (x2)



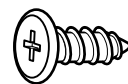
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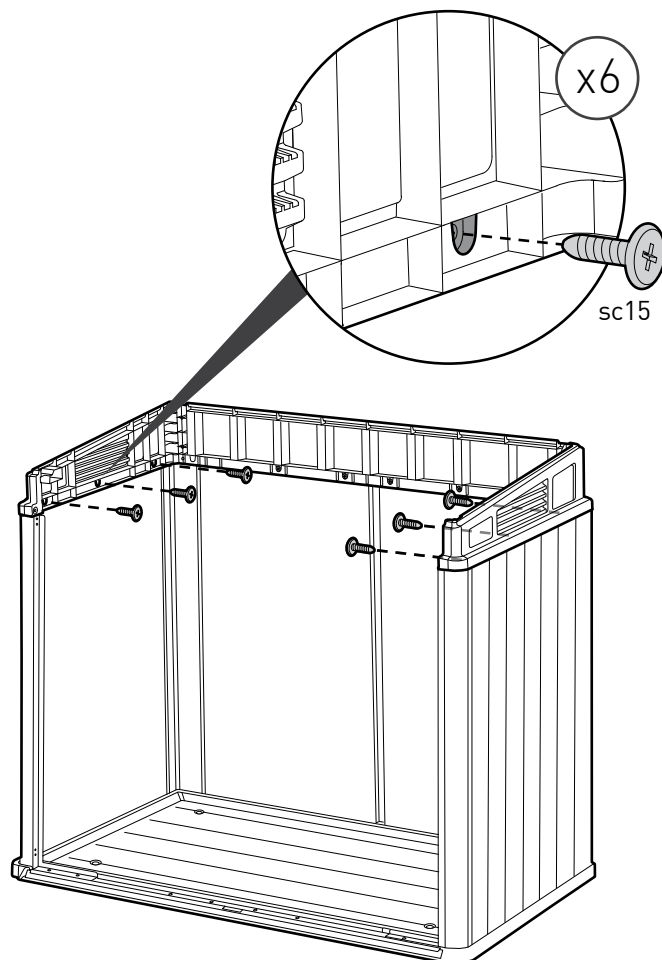
sc15 (x4)



12



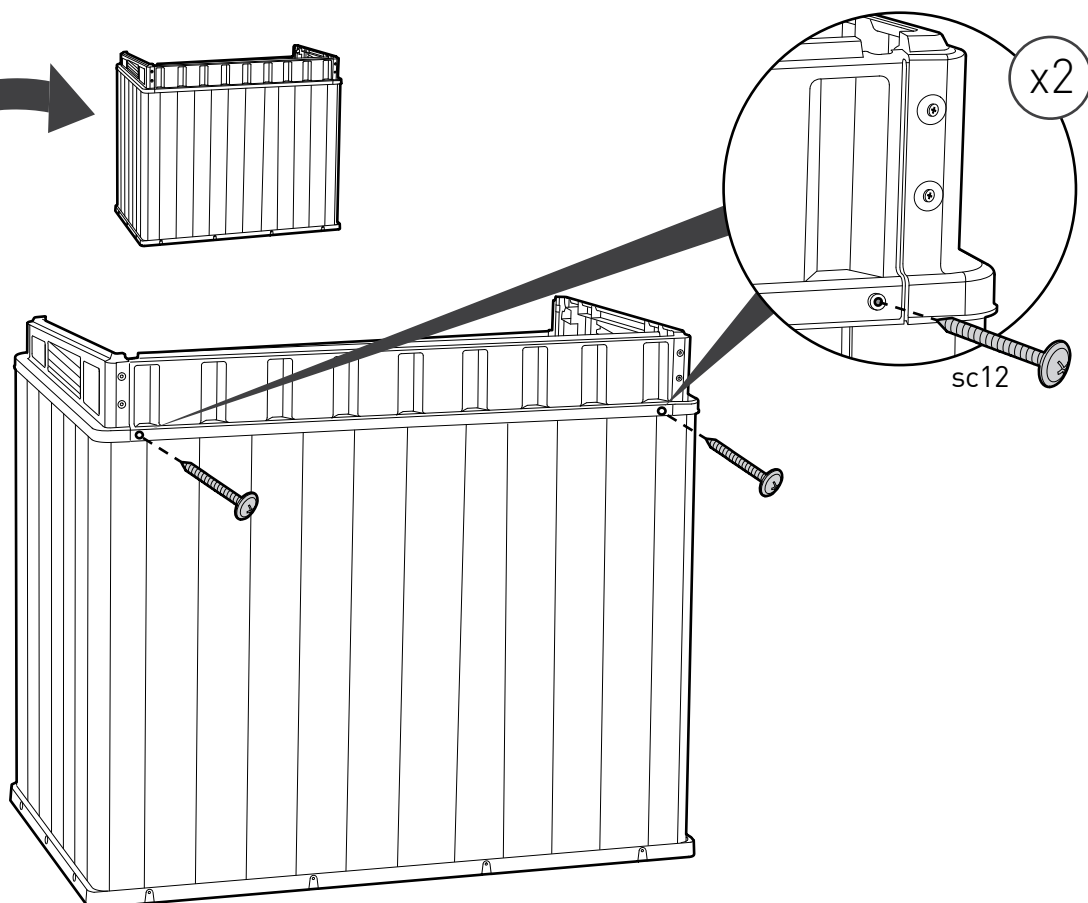
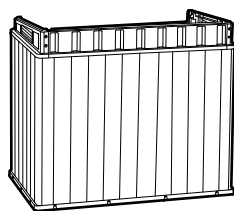
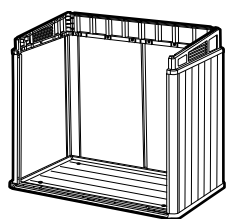
sc15 (x6)

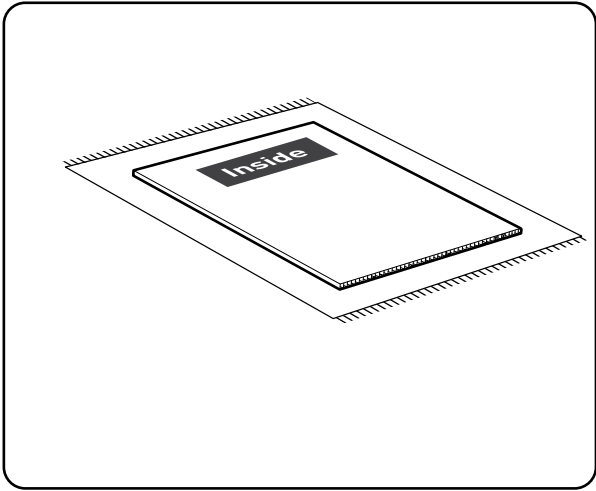
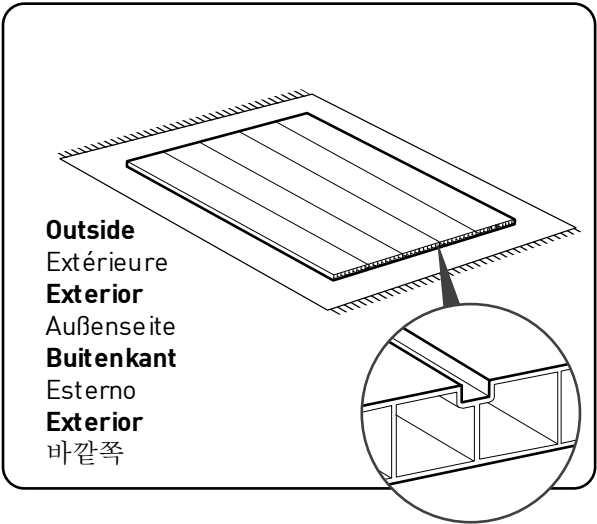


13



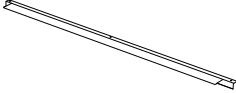
sc12 (x2)

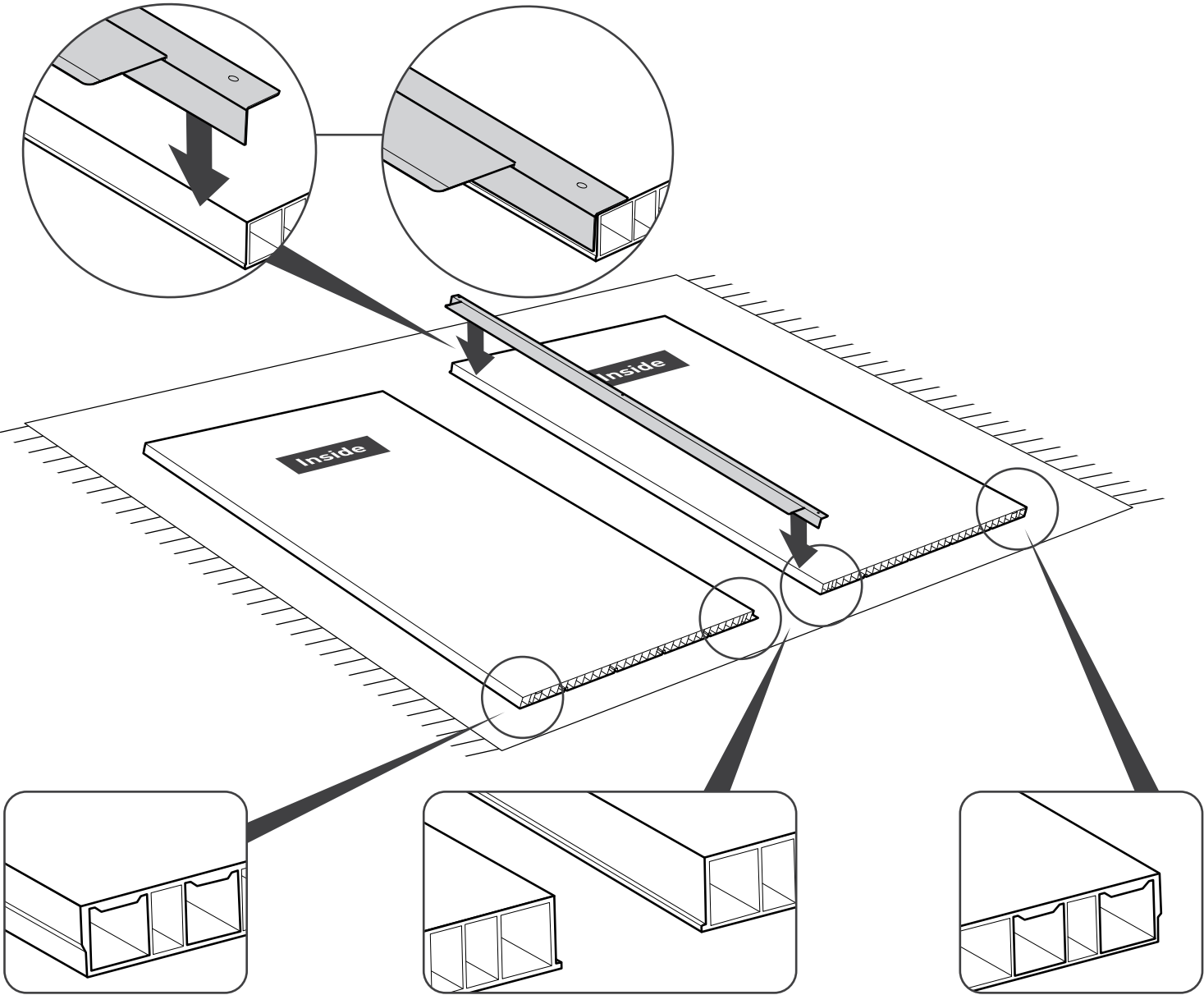


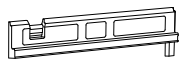


14

 ESD (x2)

 ESWN (x1)

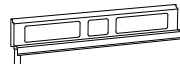




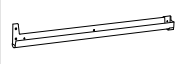
ESUDR (x1)



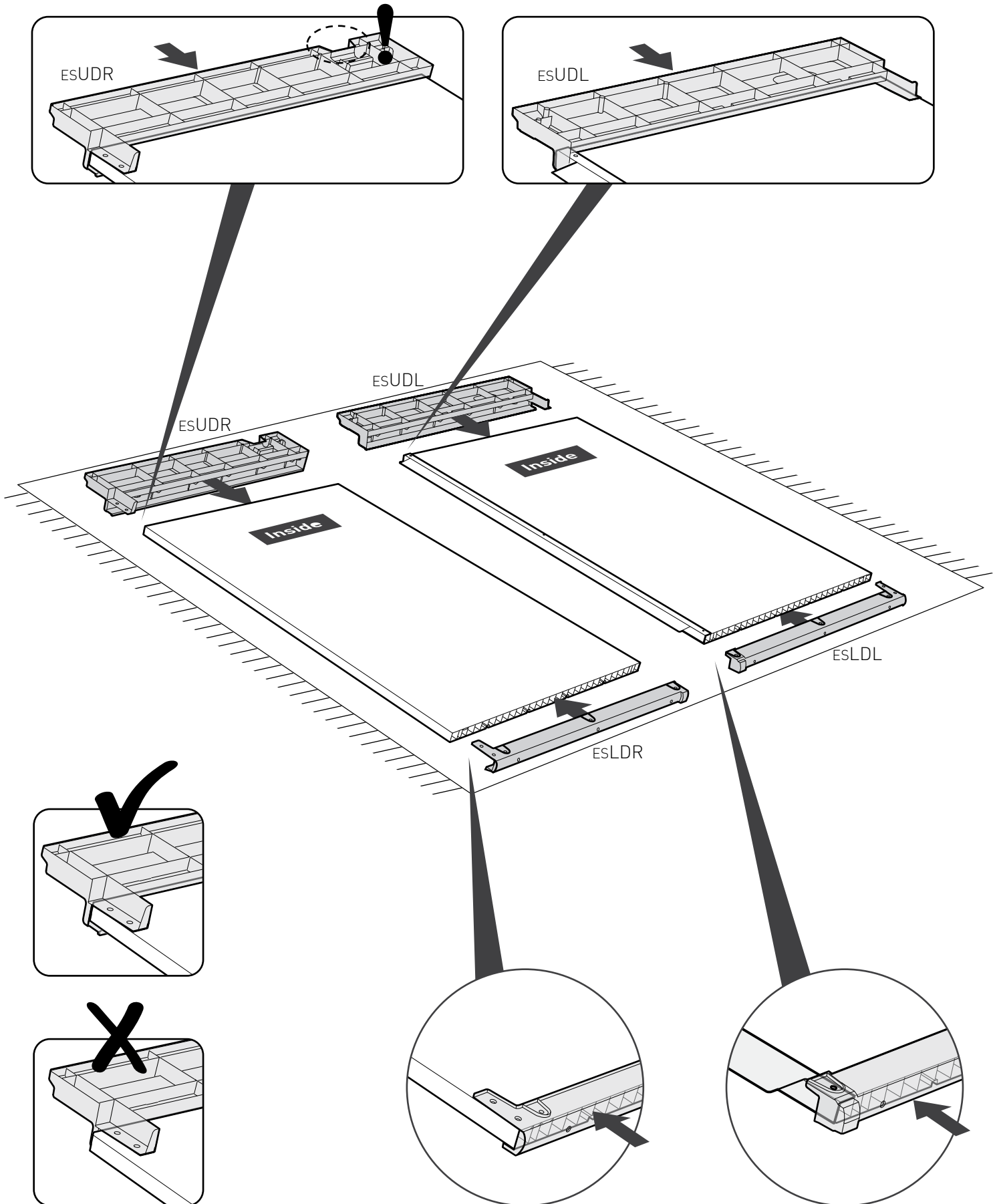
ESLDR (x1)



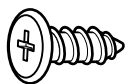
ESUDL (x1)



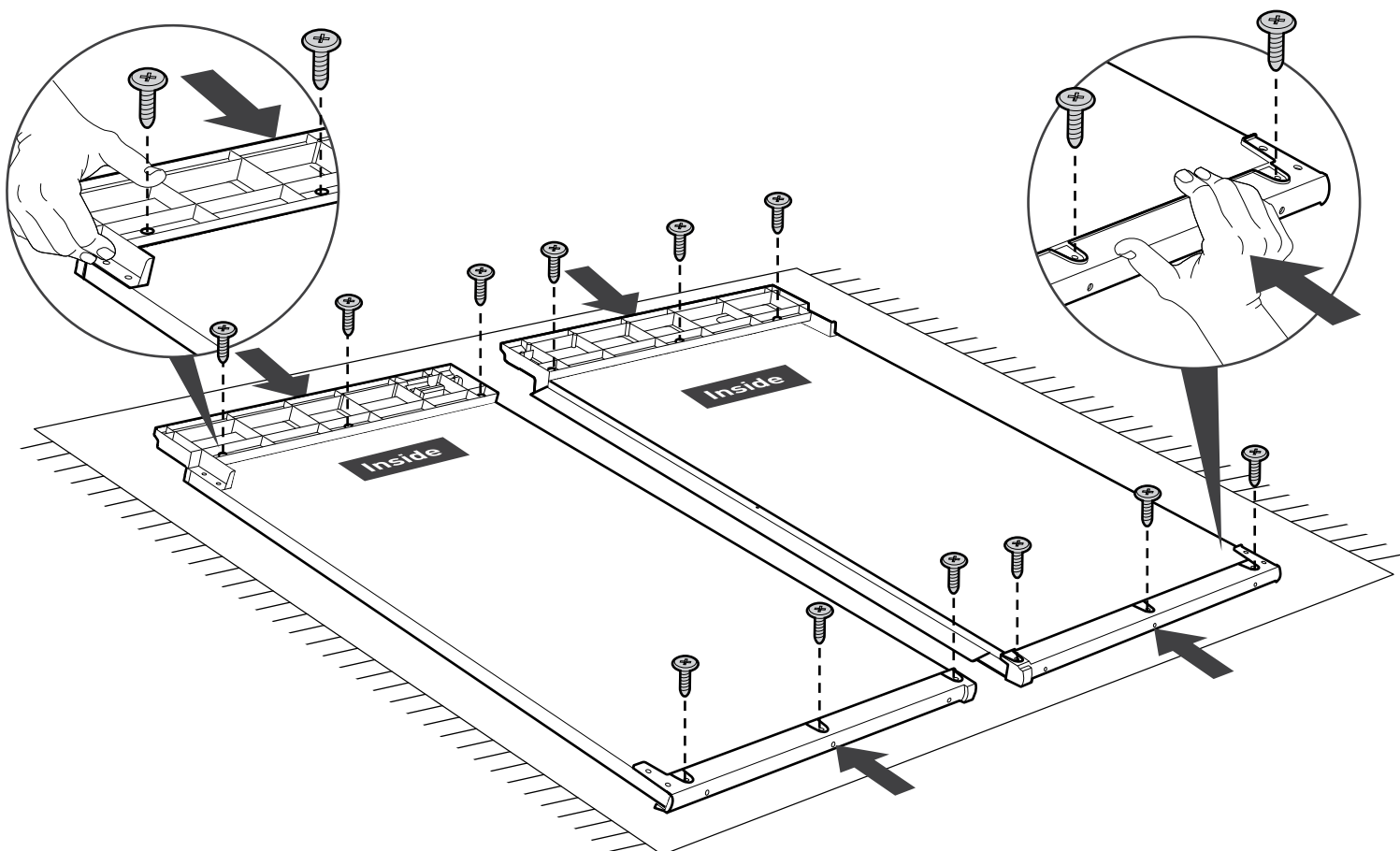
ESLDL (x1)



16



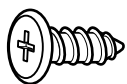
sc15 (x12)



17



ESDH (x4)



sc15 (x16)

